CLAIM AMENDMENTS

Please cancel Claims 1 and 3-6, and amend Claim 2 as follows:

- 1. (Cancelled)
- (Currently Amended) The ∆ processing apparatus according to Claim 1, comprising:

a drive pulse generator circuit for generating a drive pulse to be supplied to an image pickup element;

a wave form data supply circuit for supplying wave form setting data for generating the drive pulse to said drive pulse generator circuit at each horizontal line:

wherein the wave form setting data includes data to be set at each horizontal line and data sharing a setting area; and

wherein the wave form setting data sharing the setting area includes wave form setting data and an identification code for identifying a type of the wave form setting data, and wherein said drive pulse generator circuit includes a first detection circuit which detects the wave form setting data with reference to a horizontal synchronization signal, and a second detection circuit which detects the wave form setting data in accordance with the identification code.

3.-6. (Cancelled)